

**IN THE UNITED STATES DISTRICT COURT  
FOR THE EASTERN DISTRICT OF TEXAS  
MARSHALL DIVISION**

BLAX CORPORATION,	)	
	)	
Plaintiff	)	
	)	Civil Action No. <u>2-05CV-184 (TJW)</u>
v	)	
	)	
INTEL CORPORATION,	)	<b>JURY</b>
	)	
Defendant.	)	

**DEFENDANT INTEL CORPORATION'S ANSWER AND COUNTERCLAIMS**

Defendant Intel Corporation ("Intel") answers the First Amended Complaint of plaintiff  
BLAX Corporation ("BLAX") as follows:

**THE PARTIES**

1. Intel is without knowledge or information sufficient to form a belief as to the allegations of Paragraph 1, and so denies them.
2. Intel admits the allegations of Paragraph 2.
3. Intel admits that it manufactures microprocessors and sells and offers to sell the same in the United States. Except as expressly admitted, Intel denies that it manufactures microprocessors or sells or offers to sell them in this district.
4. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 4, and so denies them.
5. Intel is without knowledge or information sufficient to form a belief as to the allegations of Paragraph 5, and so denies them.

**JURISDICTION AND VENUE**

6. Intel admits that the First Amended Complaint alleges an action for patent infringement arising under the Patent Laws of the United States. Except as expressly admitted, Intel denies the allegations of Paragraph 6.

7. Intel admits the allegations of Paragraph 7.

8. Intel admits, for the purposes of this litigation only, the allegations of Paragraph 8.

9. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 9, and so denies them.

10. Intel admits, for the purposes of this litigation only and as to itself only, the allegation that venue is proper under 28 U.S.C. § 1400(b). Except as expressly admitted, Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 10, and so denies them.

**COUNT ONE**

11. Intel incorporates its answers to Paragraphs 1-10 as if fully set forth herein.

12. Intel admits that, on its face, U.S. Patent No. 4,847,755 (the “’755 patent”), entitled “Parallel Processing Method And Apparatus For Increasing Processing Throughput By Parallel Processing Low Level Instructions Having Natural Concurrencies,” indicates that it was issued by the United States Patent and Trademark Office (“PTO”) on July 11, 1989. Intel admits that, on its face, the ’755 patent lists Gordon E. Morrison, Christopher B. Brooks, and Frederick G. Gluck as the inventors. Intel denies that the ’755 patent was duly or legally issued. Except as expressly admitted, Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 12, and so denies them.

13. Intel denies the allegations of Paragraph 13.

14. Intel denies the allegations of Paragraph 14.

15. Intel denies the allegations of Paragraph 15.

16. Intel denies the allegations of Paragraph 16.

17. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 17, and so denies them.

18. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 18, and so denies them.

19. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 19, and so denies them.

### **COUNT TWO**

20. Intel incorporates its answers to Paragraphs 1-10 as if fully set forth herein.

21. Intel admits that, on its face, U.S. Patent No. 5,021,945 (the "'945 patent"), entitled "Parallel Processor System For Processing Natural Concurrencies And Method Therefor," indicates that it was issued by the PTO on June 4, 1991. Intel admits that, on its face, the '945 patent lists Gordon E. Morrison, Christopher B. Brooks, and Frederick G. Gluck as the inventors. Intel denies that the '945 patent was duly or legally issued. Except as expressly admitted, Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 21, and so denies them.

22. Intel denies the allegations of Paragraph 22.

23. Intel denies the allegations of Paragraph 23.

24. Intel denies the allegations of Paragraph 24.

25. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 25, and so denies them.

26. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 26, and so denies them.

27. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 27, and so denies them.

### **COUNT THREE**

28. Intel incorporates its answers to Paragraphs 1-3, 6-8, and 10 as if fully set forth herein.

29. Intel admits that, on its face, U.S. Patent No. 5,517,628 (the “628 patent”), entitled “Computer with Instructions that Use an Address Field to Select Among Multiple Condition Code Registers,” indicates that it was issued by the PTO on May 14, 1996. Intel admits that, on its face, the ‘628 patent lists Gordon E. Morrison, Christopher B. Brooks, and Frederick G. Gluck as the inventors. Intel denies that the ‘628 patent was duly or legally issued. Except as expressly admitted, Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 29, and so denies them.

30. Intel denies the allegations of Paragraph 30.

31. Intel denies the allegations of Paragraph 31.

32. Intel denies the allegations of Paragraph 32.

### **COUNT FOUR**

33. Intel incorporates its answers to Paragraphs 1-3, 6-8, and 10 as if fully set forth herein.

34. Intel admits that, on its face, U.S. Patent No. 6,253,313 (the “’313 patent”), entitled “Parallel Processor System for Processing Natural Concurrencies and Method Therefor,” indicates that it was issued by the PTO on June 26, 2001. Intel admits that, on its face, the ’313 patent lists Gordon E. Morrison, Christopher B. Brooks, and Frederick G. Gluck as the inventors. Intel denies that the ’313 patent was duly or legally issued. Except as expressly admitted, Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 34, and so denies them.

35. Intel denies the allegations of Paragraph 35.

36. Intel denies the allegations of Paragraph 36.

37. Intel denies the allegations of Paragraph 37.

#### **COUNT FIVE**

38. Intel incorporates its answers to Paragraphs 1, 4-7, and 9-10 as if fully set forth herein.

39. Intel admits that, on its face, U.S. Patent No. 5,765,037 (the “’037 patent”), entitled “System for Executing Instructions with Delayed Firing Times,” indicates that it was issued by the PTO on June 9, 1998. Intel admits that, on its face, the ’037 patent lists Gordon Edward Morrison, Christopher Bancroft Brooks, and Frederick George Gluck as the inventors. Except as expressly admitted, Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 39, and so denies them.

40. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 40, and so denies them.

41. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 41, and so denies them.

42. Intel is without knowledge or information sufficient to form a belief as to the truth of the allegations of Paragraph 42, and so denies them.

### **AFFIRMATIVE DEFENSES**

#### **First Defense: Non-Infringement**

Intel has not infringed and does not infringe, directly or indirectly, willfully or otherwise, any valid claim or claims of the '755 patent, the '945 patent, the '628 patent, or the '313 patent (collectively, the "patents-in-suit").

#### **Second Defense: Invalidity**

The patents-in-suit are invalid for failure to meet one or more of the conditions of patentability specified in 35 U.S.C. §§ 101 *et seq.*, including but not limited to 35 U.S.C. §§ 102, 103, 112, and/or under the doctrine of obviousness-type double patenting.

#### **Third Defense: Prosecution History Estoppel**

BIAX is precluded under the doctrine of prosecution history estoppel from asserting any construction of any of the claims of the patents-in-suit to cover any product manufactured, sold, or offered for sale by Intel.

#### **Fourth Defense: Laches, Waiver, and Estoppel**

The patents-in-suit are unenforceable under the doctrine of laches, waiver, and estoppel.

#### **Fifth Defense: Prosecution Laches**

The patents-in-suit are unenforceable under the doctrine of prosecution laches.

#### **Sixth Defense: Inequitable Conduct**

Some or all of the patents in suit are unenforceable because BIAX acted inequitably in prosecuting them before the United States Patent and Trademark Office ("PTO"). The acts, omissions, and misrepresentations constituting inequitable conduct included BIAX's failure to

disclose material prior art and material information in connection with the prosecution of U.S. Patent Application Serial No. 08/480,691 (the "'691 application"), which issued into the '313 patent, in violation of the applicant's duty to prosecute patent applications in the PTO with candor, good faith, and honesty. 37 C.F.R. § 1.56. Upon information and belief, such failure of disclosure was done with an intent to deceive or mislead the PTO.

On June 8, 1995, the GATT Uruguay Round Agreements Act ("URAA") became effective, limiting patents issuing from all applications filed on that date and thereafter to no longer than 20-year terms calculated from the filing date of the earliest-filed application from which such patents take priority. On June 7, 1995, the day before the URAA became effective, BIAX filed multiple divisional applications of then-pending U.S. Patent Application Serial No. 08/254,687, including the '691 application, without immediately proposing any new claims for examination.

The filing of the '691 application before the URAA effective date allowed BIAX to reserve a patent application benefiting from a pre-URAA filing date, for years before BIAX even knew what proposed claims it wanted to prosecute in it. Indeed, it was not until two years later, on or about May 27, 1997, that BIAX first submitted new proposed claims for examination in the '691 application. BIAX's newly proposed claims of the '691 application were numbered 74-98.

The patent examiner subjected proposed claims 74-98 to a restriction requirement and deemed them non-elected. In response to that action, BIAX filed another application, as a divisional of the '691 application, in order to circumvent the restriction requirement. This was U.S. Patent Application Serial No. 08/480,077 (the "'077 application"). The claims proposed in the '077 application (also numbered 74-98) were identical to the corresponding claims in the '691 application.

Patent Examiner William Treat rejected Claims 74-98 of the '077 Application as unpatentable. Among other things, he found that they were unpatentable over prior art including (1) U.S. Patent No. 4,742,453, entitled "Pipeline-Controlled Information Processing System for Generating Updated Condition Code," issued to Shibuya ("Shibuya"); (2) H. Schorr, "*Design Principles for a High-Performance System*," Proc. of the Symposium on Computers and Automata, New York, NY, April 13-15, 1971, Polytechnic Press of the Polytechnic Institute of Brooklyn, NY ("Schorr"); and (3) P. Kogge, "*The Architecture of Pipelined Computers*," Hemisphere Publishing Corp., 1981 ("Kogge"). Despite repeated efforts by the applicant to convince Examiner Treat that proposed Claims 74-98 were patentable, Examiner Treat made his rejection final on May 5, 1999, concluding that the "[a]pplicant's [latest] arguments . . . have been fully considered but they are not persuasive." BIAx was therefore fully aware of the Shibuya and Kogge references and their materiality.

On or about September 17, 1999, BIAx succeeded in overturning the restriction requirement on the proposed claims 74-98 of the '691 application. At this point in time, the PTO assigned a different patent examiner, Examiner Meng-Ai An, to examine all of the claims proposed in the '691 application on their merits, including claims 74-98. BIAx failed to disclose to Examiner An that proposed claims 74-98 of the '691 application were identical to proposed claims 74-98 of the '077 application which Examiner Treat rejected as unpatentable. Examiner An was not aware of the rejection by Examiner Treat of the claims in the '077 application and, upon information and belief, BIAx intentionally failed to disclose that information to her. To remove Examiner Treat from the picture, BIAx abandoned the '077 application, effectively terminating Examiner Treat's involvement.



Not only did BIAx fail to tell Examiner An that the claims had been rejected as unpatentable in a co-pending application, but BIAx compounded the omission by failing to disclose to Examiner An the Shibuya and Kogge references which Examiner Treat had relied upon. Upon information and belief, BIAx failed to disclose the history of the '077 application and prior art in order to deceive or mislead the Patent Office. BIAx's lead patent attorney was the attorney of record for both the '077 and '691 applications and had full knowledge of Examiner Treat's rejection of the claims.

BIAx failed to disclose additional prior art to Examiner An in connection with the prosecution of the '313 patent. Upon information and belief, during the prosecution of the '691 application, BIAx was aware of the existence of two IBM Technical Disclosure Bulletins authored by T.K.M. Agerwala. These Bulletins were entitled "*New Condition Code and Branch Architecture for High Performance Processors*" and "*Improved Condition Code and Branch Handling for Model 91-Like Implementation of the IBM System/370 Architecture*," dated June 1982 (collectively, "Agerwala"). The Agerwala references anticipate and/or render obvious the claims of the patents in suit. BIAx did not disclose these material prior art references to either Examiner An or Examiner Treat.

A reasonable patent examiner would have considered the prosecution history of the '077 application, particularly Examiner Treat's rejections, material to the patentability of the claims in the '313 patent. A reasonable patent examiner also would have considered the undisclosed prior art material to the patentability of the claims in the '313 patent. The undisclosed prior art was not cumulative of any other prior art of record in the prosecution of the '691 application. Upon information and belief, BIAx failed to disclose the aforementioned information to the examiner of the '691 application with an intent to deceive or mislead the examiner, in direct contravention

of the rules of the PTO and BIAX's duty to prosecute patent applications with candor, good faith, and honesty.

**Seventh Defense: Failure to Mark**

Prior to receiving a copy of the complaint in this action, Intel had neither actual nor constructive notice of the patents-in-suit. Upon information and belief, neither BIAX nor BIAX's licensees mark devices that embody any of the purported claims of the patents-in-suit with proper notice of such patents, as required by 35 U.S.C. § 287, such that BIAX is not entitled to any pre-filing damages pursuant to that provision.

**JURY DEMAND**

Intel requests a trial by jury as to all issues so triable.

**PRAYER FOR RELIEF**

WHEREFORE, defendant Intel prays for relief as follows:

- A. That the Court enter judgment in favor of Intel, and against plaintiff BIAX;
- B. That the Court find the patents-in-suit not infringed by Intel;
- C. That the Court find the patents-in-suit invalid;
- D. That the Court find the patents-in-suit unenforceable;
- E. That BIAX take nothing by its First Amended Complaint against Intel;
- F. That the Court deny any and all of plaintiff BIAX's requests for injunctive relief;
- G. That the Court deny any and all of plaintiff BIAX's requests for equitable relief;
- H. That the Court dismiss BIAX's First Amended Complaint in its entirety, with prejudice;

I. That the Court find this case exceptional under 35 U.S.C. § 285, and award Intel its costs and fees in this action, including reasonable attorneys' fees and pre-judgment interest thereon; and,

J. That the Court grant Intel such other and further relief as it deems just and proper.

### **COUNTERCLAIMS**

For its counterclaims against BIAX, Intel alleges as follows:

1. Intel incorporates what is set out in the preceding paragraphs as if fully set forth herein.
2. The Complaint and First Amended Complaint have established an actual and justiciable controversy between Intel and BIAX with respect to the validity, enforceability, and infringement, or lack thereof, of U.S. Patent No. 4,847,755 (the “755 patent”), U.S. Patent No. 5,021,945 (the “945 patent”), U.S. Patent No. 5,517,628 (the “628 patent”), and U.S. Patent No. 6,253,313 (the “313 patent”) (collectively, the “patents-in-suit”).
3. Jurisdiction over the subject matter of the controversy, personal jurisdiction over BIAX, and venue are co-extensive with the First Amended Complaint.

### **COUNT ONE**

#### **(Declaratory Judgment of Non-Infringement of the Patents-in-Suit)**

4. Intel incorporates Paragraphs 1-3 of these Counterclaims as is fully set forth herein.
5. Intel has not infringed and does not infringe, directly or indirectly, willfully or otherwise, any valid claim or claims of the patents-in-suit. Additionally, BIAX is precluded under the doctrine of prosecution history estoppel from asserting any construction of any of the claims of the patents-in-suit to cover any product manufactured, sold, or offered for sale by Intel.

### **COUNT TWO**

#### **(Declaratory Judgment of Invalidity of the Patents-in-Suit)**

6. Intel incorporates Paragraphs 1-3 of these Counterclaims as is fully set forth herein.

7. The patents-in-suit are invalid for failure to meet one or more of the conditions of patentability specified in 35 U.S.C. §§ 101 *et seq.*, including but not limited to 35 U.S.C. §§ 102, 103, 112, and/or under the doctrine of obviousness-type double patenting.

### **COUNT THREE**

#### **(Declaratory Judgment of Unenforceability of the Patents-in-Suit)**

8. Intel incorporates Paragraphs 1-3 of these Counterclaims as is fully set forth herein.

9. The patents-in-suit are unenforceable under the doctrine of laches, waiver, estoppel, and prosecution laches.

10. Additionally, some or all of the patents in suit are unenforceable because BIAx acted inequitably in prosecuting them before the United States Patent and Trademark Office (“PTO”). The acts, omissions, and misrepresentations constituting inequitable conduct included BIAx’s failure to disclose material prior art and material information in connection with the prosecution of U.S. Patent Application Serial No. 08/480,691 (the “’691 application”), which issued into the ’313 patent, in violation of the applicant’s duty to prosecute patent applications in the PTO with candor, good faith, and honesty. 37 C.F.R. § 1.56. Upon information and belief, such failure of disclosure was done with an intent to deceive or mislead the PTO.

11. On June 8, 1995, the GATT Uruguay Round Agreements Act (“URAA”) became effective, limiting patents issuing from all applications filed on that date and thereafter to no longer than 20-year terms calculated from the filing date of the earliest-filed application from which such patents take priority. On June 7, 1995, the day before the URAA became effective, BIAx filed multiple divisional applications of then-pending U.S. Patent Application Serial No.

08/254,687, including the '691 application, without immediately proposing any new claims for examination.

12. The filing of the '691 application before the URAA effective date allowed BIAx to reserve a patent application benefiting from a pre-URAA filing date, for years before BIAx even knew what proposed claims it wanted to prosecute in it. Indeed, it was not until two years later, on or about May 27, 1997, that BIAx first submitted new proposed claims for examination in the '691 application. BIAx's newly proposed claims of the '691 application were numbered 74-98.

13. The patent examiner subjected proposed claims 74-98 to a restriction requirement and deemed them non-elected. In response to that action, BIAx filed another application, as a divisional of the '691 application, in order to circumvent the restriction requirement. This was U.S. Patent Application Serial No. 08/480,077 (the "'077 application"). The claims proposed in the '077 application (also numbered 74-98) were identical to the corresponding claims in the '691 application.

14. Patent Examiner William Treat rejected Claims 74-98 of the '077 Application as unpatentable. Among other things, he found that they were unpatentable over prior art including: (1) U.S. Patent No. 4,742,453, entitled "Pipeline-Controlled Information Processing System for Generating Updated Condition Code," issued to Shibuya ("Shibuya"); (2) H. Schorr, *"Design Principles for a High-Performance System,"* Proc. of the Symposium on Computers and Automata, New York, NY, April 13-15, 1971, Polytechnic Press of the Polytechnic Institute of Brooklyn, NY ("Schorr"); and (3) P. Kogge, *"The Architecture of Pipelined Computers,"* Hemisphere Publishing Corp., 1981 ("Kogge"). BIAx was therefore fully aware of the Shibuya and Kogge references and their materiality. Despite repeated efforts by the applicant to convince

Examiner Treat that proposed Claims 74-98 were patentable, Examiner Treat made his rejection final on May 5, 1999, concluding that the “[a]pplicant’s [latest] arguments . . . have been fully considered but they are not persuasive.” BIAx was therefore fully aware of the Shibuya and Kogge references and their materiality.

15. On or about September 17, 1999, BIAx succeeded in overturning the restriction requirement on the proposed claims 74-98 of the ‘691 application. At this point in time, the PTO assigned a different patent examiner, Examiner Meng-Ai An, to examine all of the claims proposed in the ‘691 application on their merits, including claims 74-98. BIAx failed to disclose to Examiner An that proposed claims 74-98 of the ‘691 application were identical to proposed claims 74-98 of the ‘077 application which Examiner Treat rejected as unpatentable. Examiner An was not aware of the rejection by Examiner Treat of the claims in the ‘077 application and, on information and belief, BIAx intentionally failed to disclose that information to her. To remove Examiner Treat from the picture, BIAx abandoned the ‘077 application, effectively terminating Examiner Treat’s involvement.

16. Not only did BIAx fail to tell Examiner An that the claims had been rejected as unpatentable in a co-pending application, but BIAx compounded the omission by failing to disclose to Examiner An the Shibuya and Kogge references which Examiner Treat had relied upon. Upon information and belief, BIAx failed to disclose the history of the ‘077 application and prior art in order to deceive or mislead the Patent Office. BIAx’s lead patent attorney was the attorney of record for both the ‘077 and ‘691 applications and had full knowledge of Examiner Treat’s rejection of the claims.

17. BIAx failed to disclose additional prior art to Examiner An in connection with the prosecution of the ‘313 patent. Upon information and belief, during the prosecution of the

'691 application, BIAx was aware of the existence of two IBM Technical Disclosure Bulletins authored by T.K.M. Agerwala. These Bulletins were entitled "*New Condition Code and Branch Architecture for High Performance Processors*" and "*Improved Condition Code and Branch Handling for Model 91-Like Implementation of the IBM System/370 Architecture*," dated June 1982 (collectively, "Agerwala"). The Agerwala references anticipate and/or render obvious the claims of the patents in suit. BIAx did not disclose these material prior art references to either Examiner An or Examiner Treat.

18. A reasonable patent examiner would have considered prosecution the history of the '077 application, particularly the rejections, material to the patentability of the claims in the '313 patent. A reasonable patent examiner also would have considered the undisclosed prior art material to the patentability of the claims in the '313 patent. The undisclosed prior art was not cumulative of any other prior art of record in the prosecution of the '691 application. Upon information and belief, BIAx failed to disclose the aforementioned information to the examiner of the '691 application with an intent to deceive or mislead the Examiner, in direct contravention of the rules of the PTO and BIAx's duty to prosecute patent applications with candor, good faith, and honesty.

#### **JURY DEMAND**

Intel requests a trial by jury as to all issues so triable.

#### **PRAYER FOR RELIEF**

WHEREFORE, Counterclaimant Intel prays for relief as follows:

- A. That the Court enter judgment in favor of Intel, and against plaintiff BIAx;
- B. That the Court enter declaratory judgment that the patents-in-suit are not infringed by Intel;



- C. That the Court enter declaratory judgment that the patents-in-suit are invalid;
- D. That the Court enter declaratory judgment that the patents-in-suit are unenforceable;
- E. That BIAx take nothing by its First Amended Complaint against Intel;
- F. That the Court deny any and all of plaintiff BIAx's requests for injunctive relief;
- G. That the Court deny any and all of plaintiff BIAx's requests for equitable relief;
- H. That the Court dismiss BIAx's First Amended Complaint in its entirety, with prejudice;
- I. That the Court find this case exceptional under 35 U.S.C. § 285, and award Intel its costs and fees in this action, including reasonable attorneys' fees and pre-judgment interest thereon; and,
- J. That the Court grant Intel such other and further relief as it deems just and proper.

Dated: August 3, 2005

Respectfully submitted,

Harry Lee Gillam, Jr., *Attorney In Charge*  
State Bar No.: 07921800  
Gillam & Smith LLP  
110 S. Bolivar, Suite 204  
Marshall, TX 75670  
Tel. (903) 934-8450  
Fax. (903) 934-9257  
Email: [gil@gillamsmithlaw.com](mailto:gil@gillamsmithlaw.com)

Eric Findlay  
State Bar No.: 00789886  
Ramey & Flock PC  
100 E. Ferguson, Suite 500  
Tyler, TX 75702-0629  
Tel. (903) 597-3301  
Fax (903) 597-2413  
Email: [ericf@rameyflock.com](mailto:ericf@rameyflock.com)



Ronald S. Vickery  
State Bar No.: 20574250  
Ramey & Flock PC  
100 E. Ferguson, Suite 500  
Tyler, TX 75702-0629  
Tel. (903) 597-3301  
Fax (903) 597-2413  
Email: [ronv@rameyflock.com](mailto:ronv@rameyflock.com)

OF COUNSEL:

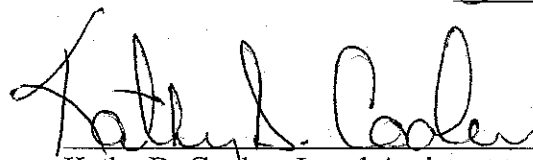
Chris R. Ottenweller  
G. Hopkins Guy III  
Cynthia A. Wickstrom  
Ulysses S. Hui  
Brian H. VanderZanden  
Orrick, Herrington & Sutcliffe LLP  
1000 Marsh Road  
Menlo Park, CA 94025  
Tel. (650) 614-7400

Lisa C. Ward  
Orrick, Herrington & Sutcliffe LLP  
4 Park Plaza  
Irvine, CA 92614-2558  
Tel. (949) 567-6700

*Attorneys for Intel Corporation*

**CERTIFICATE OF SERVICE**

The undersigned hereby certified that the foregoing document was filed electronically in compliance with Local Rules CV-5(a). As such, this answer was served on all counsel pursuant to Local Rules CV-5(a)(3)(A) and the Federal Rules of Civil Procedure on this the 3<sup>rd</sup> day of August, 2005.

  
Kathy D. Cooley, Legal Assistant to Ronald  
S. Vickery